Edon $-\mathcal{R}$, An Infinite Family of Cryptographic Hash Functions

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Abstract

We propose a new infinite family of cryptographic hash functions, Edon $-\mathcal{R}$, based on a recently defined candidate one-way function. Edon $-\mathcal{R}$ is a class of hash functions with variable output lengths. It is defined using quasi-groups and quasigroup string transformations.

Keywords: Hash function, one-way function, quasigroup

1 Introduction

This is an updated version of the paper [9] with the same title that was presented at the Second NIST Cryptographic Hash Workshop that was held in August, 2006 in University of California - Santa Barbara, USA.

Cryptographic hash functions are used in a wide range of applications including message integrity, authentication, digital signature and public key encryption. A cryptographic hash function takes an input of arbitrary size and produces an output, also called the hash value, of a fixed, predetermined size. The important properties of a cryptographic hash function are the collision-resistance and the preimage-resistance. Most hash functions are iterative and are built using the Merkle-Damgård design of hash functions [5, 21]. Although the mathematical knowledge about practical construction of cryptographic hash functions was increasing with slow rate during the last 15–20 years, we can locate at least four breakthroughs in their theoretical understanding. Those are given in the following list:

1) The first result from 1989 is that of Merkle [21] and Damgård [5] where they proved that for the iterated hash function h to be collision resistant it is sufficient that its compression function f is a collision resistant function.

- 2) The second theoretical result from 1992 is due to Lai and Massey in [15] where they proved that finding second preimages of some iterated hash function h(having a compression function f in its design) and by fixed initial IV in less than 2^n operations is equivalent of finding second preimages of the compression function f with arbitrary IV in less than 2^n operations.
- 3) The third important theoretical result is that of Joux [13] in 2004 were he showed that the workload for finding second-preimage collisions with equal length for iterated one-way hash functions is about $\log(k) \times 2^{n/2}$, where k is the number of computed hash values.
- 4) The fourth theoretical result from 2005 is that of Kelsy and Schneier in [14] where they showed that the workload for finding second-preimages that are expandable messages with different length is about $k \times 2^{n/2+1}$.

Concerning the Merkle-Damgård design, Coron et al. in [3] and Lucks in [16] made several suggestions how to strengthen that design while Gauravaram, Millan and Neito in a recent ePrint paper [6] gave an interesting discussion on the possibilities that Merkle-Damgård design for MDx family was, in fact, not properly implemented.

Although general in their approach, the above mentioned breakthroughs in the analysis of iterated hash functions are connected with the latest successes in the cryptanalysis of MD4 family of hash functions [30]. This family of hash functions was being in use around 15 years and it is one of the most used cryptographic primitives. Recently, Wang et al. [33] pointed out some weaknesses of SHA-1 hash function. Many researchers immediately proposed SHA-1 to be replaced by SHA-256 (which outputs 256 bits instead of 160 bits of SHA-1). Other candidates from SHA family include SHA-n, where $n \in 224, 256, 384, 512$. We can mention here several other members of MDx family: RIPEMD-160 [25], as well as RIPEMD-256 and RIPEMD-320 [28].

To have in a reserve hash functions with bigger output was a strategy that was adopted from several international standardization organizations (including NIST and ISO). However, the costs of transition from one function to another are enormous, and the time for introducing a new hash function is estimated from 5 to 10 years.

In this paper we support the concept of designing cryptographic hash functions with variable output lengths. One such hash function with variable hash lengths, but restricted to 128, 160, 192, 224 or 256 output bits is HAVAL, proposed in 1993 by Zheng, Pieprzyk and Seberry [34]. However, the principles of the design of HAVAL are similar to those of MDx family of hash functions and collisions were found for 128 bit version of the function by Wang et al. in [32]. Our design allows hash digest sizes with 160, 256, 512, 1024, 2048, or any other number of bits. As far as we know by that property our design is unique among proposed cryptographic hash functions. Moreover, that property becomes even more important from the perspective of ever increasing computing power. Namely, attacks with complexity 2^{64} are already accessible using distributed internet computing, while attacks with complexity 2^{80} may also be feasible after 5–10 years. Therefore, hash functions with output size less than 160 bits are not a good choice for long term security. The ever-growing computing power is forcing designers to redesign secure hash functions, which is not at all trivial designing process. Instead of redesigning and creating new secure hash functions with larger (but still fix) digest size, we advocate for the design of hash functions with variable length of output. As an additional argument for our standing we can mention the analogy with RSA algorithm [29] and its usage during the last 20 years. As the computing power was increasing, the length of prime numbers used in RSA was increasing, but the basic algorithm is still unchanged.

The design of a cryptographically secure hash function is a sufficient motivation and challenging problem by itself, but we had additional motive in the light of the last visions and revisions on Merkle-Damgård design. Actually, the number of cryptographically secure hash functions is not very large – see for example the excellent review of Preneel [26] or visit the web page [27] where you can find a good and updated review of almost all known secure hash functions.

From the point of view of used mathematical techniques, our design can be treated as one of those designs which does not rely on ad-hock techniques using XOR-ing and rotating, but tries to relate the claims of their security on some mathematically hard problem. Our design is based on theory of quasigroups and quasigroup string transformations [8, 19] and recently introduced one-way candidate functions [10]. The main point is the hardness of solving non-linear systems of quasigroup equations, since quasigroups are algebraic structures with one binary operation, which do not satisfy the usual algebraic laws used in solving equations (the commutative law, the associative law, the idempotent law, having zeros or units, and so on).

A similar approach have been taken by Damgård in 1988 [4] and Gibson in 1991 [7], who designed hash functions based on intractability of discrete log problem, as well as recently by Contini, Lenstra and Steinfeld in 2005 [2], who proposed a hash function which cryptographic strength relies on the hardness of the number factorization problem.

The organization of the paper is as follows: In Section 2 we give some basic mathematical definitions and a definition of a one-way function, in Section 3 we define the hash function Edon– \mathcal{R} , in Section 4 we analyze the cryptographic properties of the proposed hash function, and we conclude the paper by Section 5.

2 Definition of a One-Way Function $\mathcal{R}_1: Q^r \to Q^r$

In this section we need to repeat some parts of the definition of the class of one-way candidate functions \mathcal{R}_1 recently defined in [10] as well as the parts of the conjectured computational difficulties for their inversion. For that purpose we will need also several brief definitions for quasigroups and quasigroup string transformations.

A quasigroup (Q, *) is an algebraic structure consisting of a nonempty set Q and a binary operation $*: Q^2 \to Q$ with the property each of the equations a * x = b and y * a = b to have unique solutions x and y in Q. We deal with finite quasigroups only. Closely related combinatorial structures to finite quasigroups are the Latin squares, since the main body of the multiplication table of a quasigroup is just a Latin square. More detailed information about theory of quasigroups, quasigroup string processing, Latin squares and hash functions you can find in [1, 17, 18, 20].

In our design we are using finite quasigroups (Q, *) of order |Q| = n, where $n \ge 2$ and $n = 2^w$. That means that the hash functions use $|Q|^2 = n^2$, w-bit words of internal memory for storing the quasigroup. For example if we take w = 8 then $n = 2^w = 256$ and we will work with a set of 256 8-bit words (bytes), i.e., we will work with the set $Q = \{0, 1, \dots, 255\}$ which in fact is representing the ASCII set. In such a way, we will need $256^2 = 65536$ bytes, i.e., 65Kb of internal memory for storing the quasigroup, which is not small amount if we want to implement it in some embedded microprocessor. However, the design allows us to use quasigroups of any order which is a power of 2, i.e. 8, 16, 32, 64, ..., but the corresponding operations inside the hash functions will not be on bytes (8-bit words) but on different w-bit words. Then the needed internal memory for storing the quasigroup would be 24

bytes, 128 bytes, 640 bytes, 3 Kb, and so on.

For the description of the algorithm we will use the following definitions:

Definition 1. ([10] Quasigroup reverse string transformation $\mathcal{R}_1: Q^r \to Q^r)$

Let r be a positive integer, let (Q, *) be a quasigroup and $a_j, b_j \in Q$. For each fixed $m \in Q$ define first the transformation $Q_m : Q^r \to Q^r$ by

$$Q_m(a_0, a_1, \dots, a_{r-1}) = (b_0, b_1, \dots, b_{r-1})$$

$$\iff b_i := \begin{cases} m * a_0, & i = 0\\ b_{i-1} * a_i, & 1 \le i \le r-1. \end{cases}$$

Then define \mathcal{R}_1 as composition of transformations of kind Q_m , for suitable choices of the indexes m, as follows.

$$\mathcal{R}_1(a_0, a_1, \dots, a_{r-1}) := Q_{a_0}(Q_{a_1} \dots (Q_{a_{r-1}}(a_0, a_1, \dots, a_{r-1}))).$$

Definition 2. (Shapeless quasigroup) A quasigroup (Q, *) of order n is said to be shapeless if it is noncommutative, non-associative, it does not have neither left nor right unit, it does not contain proper sub-quasigroups, and there is no k < 2n for which are satisfied the identities of the kinds:

$$\underbrace{x * (\dots * (x * y))}_{k} = y, \ y = ((y * \underbrace{x) * \dots) * x}_{k}.$$
 (1)

Shapeless quasigroups can be effectively constructed by using, for example, the Hall's algorithm [12] for construction of systems of different representatives of a family of finite sets. The reason why it is possible to construct shapeless quasigroups is the huge number of quasigroups (for example, there are around 2^{430} quasigroups of order 16 and much more than 2^{192672} quasigroups of order 256). Basically, our claims that the family of hash functions Edon- \mathcal{R} that we will define in the next section is infinite relies upon the fact that the number of finite quasigroups (and shapeless quasigroups) is infinite and it grows with double exponential rate with the order of the quasigroup (see for example [20]).

We want to note that linear quasigroups that are mentioned in [10] and that are not suitable for design of quasigroup one-way functions \mathcal{R}_1 actually does not satisfy the criterion of being shapeless, concretely the criterion described by the Equation (1).

Our statement that \mathcal{R}_1 is one-way function, i.e., for a given string $A \in Q^r$ it is easy to compute $\mathcal{R}_1(A)$ and it is computationally infeasible, for a given $B \in Q^r$, to find an $A \in Q^r$ such that $B = \mathcal{R}_1(A)$, is based on the following hypothesis:

Hypothesis. There is no effective algorithm for solving a system of non-linear quasigroup equations in a shapeless quasigroup.

In this moment there is not enough mathematical knowledge for solving systems of quasigroup equations in order to prove or disprove the above hypothesis. However, our quasigroup string transformations can be seen as a special type of cellular automata operations. The predictability of cellular automata was investigated by Moore et al. in [22, 23] in cases when the obtained quasigroups have some of the properties that shapeless quasigroups don't. Moreover Goldmann and Russell [11] have shown that solving system of equations in non-abelian groups is NP-complete and Moore, Tesson and Thérien in [24] have shown NP-completeness for even more general algebraic structures, i.e., monoids that are not product of Abelian group and commutative idempotent monoid. Based on all this discussion we state the following conjecture:

Conjecture 1. In a shapeless quasigroup (Q, *), the function $\mathcal{R}_1 : Q^r \to Q^r$ is one-way function.

From the previous discussion we can say that having a shapeless quasigroup, i.e., a quasigroup that does not have any mathematical property that will help us to reduce the complexity of the equations that have to be solved, it seems that only method for solving systems of quasigroup equations is by combinatorial exhaustive search and reading from the lookup table that defines the quasigroup. Next, we give arguments that support the Conjecture 1 from the lookup table point of view, by the following Theorem (similar but slightly more concise then it is in [10]):

Theorem 1. If the quasigroup (Q, *) of order n is shapeless, then the number of computations based only on the lookup table that defines the quasigroup (Q, *) for finding a preimage of the function $\mathcal{R}_1 : Q^r \to Q^r$ is $O(n^{\lfloor \frac{r}{3} \rfloor})$.

Proof. It is clear that $\mathcal{R}_1(A)$ can be effectively computed for any string $A = (a_0, a_1, \ldots, a_{r-1}) \in Q^r$ in $O(r^2)$ operations. For the inverse task, given a string $B = (b_0, b_1, \ldots, b_{r-1}) \in Q^r$, we have to find a string $A = (x_0, x_1, \ldots, x_{r-1})$ such that

$$B = \mathcal{R}_1(A) = Q_{x_0}(Q_{x_1} \dots (Q_{x_{r-1}}(x_0, x_1, \dots, x_{r-1}))).$$

Further on, let denote $Q_{x_{r-1}}(x_0, x_1, \dots, x_{r-1}) = (x_0^{(1)}, x_1^{(1)}, \dots, x_{r-1}^{(1)})$ and $Q_{x_{r-i}}(x_0^{(i-1)}, x_1^{(i-1)}, \dots, x_{r-1}^{(i-1)}) = (x_0^{(i)}, x_1^{(i)}, \dots, x_{r-1}^{(i)})$ for $i = 2, 3, \dots r$. Then

$$B = Q_{x_0}(x_0^{(r-1)}, \dots, x_{r-1}^{(r-1)}) = (x_0^{(r)}, \dots, x_{r-1}^{(r)}),$$

i.e., $b_j = x_j^{(r)}$ for j = 0, 1, ..., r - 1. In order for clearer presentation of these computations, we use the Table 1, where $x_0^{(1)} = x_{r-1} * x_0$, $x_0^{(i+1)} = x_{r-i-1} * x_0^{(i)}$ for i = 1, 2, ..., r - 1, $x_j^{(1)} = x_{j-1}^{(1)} * x_j$ for j = 1, ..., r - 1, $x_{j+1}^{(i)} = x_{j+1}^{(i)}$ for i = 2, ..., r, j = 0, ..., r - 2. It can be seen from Table 1 and from the definition

It can be seen from Table 1 and from the definition of the transformations Q_m that we can find the solutions of the equations $b_i * x_{i+1}^{(r-1)} = b_{i+1}$ in indeterminate $x_{i+1}^{(r-1)}$, $i = 0, \ldots, r-2$, and let denote them by $b_{i+1}^{(r-1)}$. the function \mathcal{R}_1 .

		x_0	x_1	x_2		x_{r-2}	x_{r-1}
	x_{r-1}	$x_0^{(1)}$	$x_1^{(1)}$	$x_2^{(1)}$		$x_{r-2}^{(1)}$	$x_{r-1}^{(1)}$
	$\begin{array}{c} x_{r-2} \\ \vdots \\ x_2 \end{array}$	$\begin{array}{c} x_{0}^{(1)} \\ x_{0}^{(2)} \\ \vdots \\ x_{0}^{(r-2)} \\ x_{0}^{(r-1)} \end{array}$	$x_1^{(2)}$: $x_1^{(r-2)}$	$x_{2}^{(2)}$: $x_{2}^{(r-2)}$		$ \begin{array}{c} x_{r-2}^{(2)} \\ \vdots \\ x_{r-2}^{(r-2)} \end{array} $	$x_{r-1}^{(2)}$: $x_{r-1}^{(r-2)}$
	x_1	$x_0^{(r-1)}$	$x_1^{(r-1)}$	$x_2^{(r-1)}$		$x_{r-2}^{(r-1)}$	$x_{r-1}^{(r-1)}$
-	x_0	b_0	b_1	b_2	•••	b_{r-2}	b_{r-1}

After that we can find the solutions $b_{i+1}^{(r-2)}$ of the equations $b_i^{(r-1)} * x_{i+1}^{(r-2)} = b_{i+1}^{(r-1)}$ in indeterminate $x_{i+1}^{(r-2)}$ for $i = 1, \ldots, r-2$, and so on. After r-2 steps we have the solution $b_{r-1}^{(1)}$ of the equation $b_{r-2}^{(2)} * x_{r-1}^{(1)} = b_{r-1}^{(2)}$. Then Table 1 transforms to Table 2. We can see that no value for the indeterminate x_0, \ldots, x_{r-1} can be obtained.

Let choose a value a_0 for x_0 . Then, as we explained above, we can determine from Table 2 the values for above, we can determine non-large $x_0^{(r-1)}$, $x_1^{(r-2)}$, ..., $x_{r-2}^{(1)}$ and x_{r-1} and let denote them as $a_0^{(r-1)}$, $a_1^{(r-2)}$, ..., $a_{r-2}^{(1)}$ and a_{r-1} . Then we can also compute the value $x_0^{(1)} = a_{r-1} * a_0$ and let denote it by $c_0^{(1)}$. After that we can choose a value a_1 for x_1 and we can transform the Table 2 with new known values as it is shown in Table 3. It is an elementary fact from analytic geometry that the intersection will occur for $s \leq \lfloor r/3 \rfloor$. (Namely, we can interpret the main diagonal as the line y = x which is perpendicular to the minor diagonal y = a - x. Then the 'points' $\{c_0^{(1)}, c_1^{(2)}, \dots\}$ are 'laying on' the main diagonal y = x and the points $\{a_0, \dots\}$ $a_0^{(r-1)}, a_1^{(r-2)}, \ldots, a_{r-1}$, are 'laying on' the minor diagonal. For different values of $a \ (a = r - 1, ...)$ we have parallels above the minor diagonal y = a - x such as $\{a_1, d_2\}$ $a_0^{(r-2)}, a_1^{(r-3)}, \ldots, a_{r-2}$ and so on.)

After choosing $s = \lfloor \frac{r}{3} \rfloor$ values for x_0, x_1, \ldots, x_s we will reach the intersection point between the main diagonal and a parallel that lies above the minor diagonal. Then a collision can happen in fulfilling the Table 3. In fact, we have to check if several equalities of kind $c_s^{(s)}\ast c_{s+1}^{(s-1)}=a_{s+1}^{(s)},\ c_{s-1}^{(s+1)}\ast c_s^{(s)}=a_s^{(s+1)},\ c_{s+1}^{(s-1)}\ast c_{s+2}^{(s-2)}=$ $a_{s+2}^{(s-1)},\ldots$ are satisfied. (Again by the analogy with the analytic geometry representation, we have to address to one 'plane point' two values, and those values have to be equal.) There are $|Q|^{\lfloor r/3 \rfloor} = n^{\lfloor r/3 \rfloor}$ possible choices for x_0, x_1, \ldots, x_s . In average, after $\frac{1}{2}n^{\lfloor r/3 \rfloor}$ choices we will find the right choice. It follows that we have to make $\mathcal{O}(n^{\lfloor r/3 \rfloor})$ guesses for exact completing of the Table 1, i.e, for finding a pre-image $A = (a_0, a_1, \ldots, a_{r-1})$ of the given string $B = (b_0, b_1, \dots, b_{r-1})$ such that $B = \mathcal{R}(A)$.

Table 1: Tabular representation of the computations of Table 2: Straightforward computations for inverting the function \mathcal{R}_1 , from the knowledge of the vector B.

	x_0	x_1	x_2		x_{r-2}	x_{r-1}
x_{r-1}	$x_0^{(1)}$	$x_1^{(1)}$	$x_2^{(1)}$		$x_{r-2}^{(1)}$	$b_{r-1}^{(1)}$
x_{r-2} .	$x_0^{(2)}$	$x_1^{(2)}$	$x_2^{(2)}$		$b_{r-2}^{(2)}$	$b_{r-1}^{(2)}$
x_2	$\begin{vmatrix} : \\ x_0^{(r-2)} \end{vmatrix}$: $x_1^{(r-2)}$	$\stackrel{:}{_{b_2^{(r-2)}}}$:	$: \\ b_{r-2}^{(r-2)} $: $b_{r-1}^{(r-2)}$
x_1	$\begin{array}{c} x_{0}^{(1)} \\ x_{0}^{(2)} \\ \vdots \\ x_{0}^{(r-2)} \\ x_{0}^{(r-1)} \\ \vdots \end{array}$	$b_1^{(r-1)}$	$b_2^{(r-1)}$		$b_{r-2}^{(r-1)}$	$b_{r-1}^{(r-1)}$
x_0	b_0	b_1	b_2	•••	b_{r-2}	b_{r-1}

Table 3: Computations for inverting the function \mathcal{R}_1 , from the knowledge of the vector B and guessed values for a_0 and a_1 .

	a_0	a_1	x_2		$x_{r-4} x_{r-3} a_{r-2} a_{r-1}$
a_{r-1}					$x_{r-4}^{(1)} a_{r-3}^{(1)} a_{r-2}^{(1)} b_{r-1}^{(1)}$
a_{r-2}					$a_{r-4}^{(2)} a_{r-3}^{(2)} b_{r-2}^{(2)} b_{r-1}^{(2)}$
	:	:	$x_{2}^{(3)}$: $a_{2}^{(r-3)}$	· · · · : . · · ·	$a_{r-4}^{(3)} b_{r-3}^{(3)} b_{r-2}^{(3)} b_{r-1}^{(3)}$ $\vdots \qquad \vdots \qquad b_{r-4}^{(r-3)} b_{r-3}^{(r-3)} b_{r-2}^{(r-3)} b_{r-1}^{(r-3)}$
x_2	$a_0^{(r-2)}$	$a_1^{(r-1)}a_1^{(r-1)}$	$b_2^{(r-2)}$		$b_{r-4}^{(r-2)} b_{r-3}^{(r-2)} b_{r-2}^{(r-2)} b_{r-1}^{(r-2)}$
a_1	$a_0^{(r-1)}$	$b_1^{(r-1)}$	$b_2^{(r-1)}$		$b_{r-4}^{(r-1)} b_{r-3}^{(r-1)} b_{r-2}^{(r-1)} b_{r-1}^{(r-1)}$
a_0	b_0	b_1	b_2		$b_{r-3} \ b_{r-4} \ b_{r-2} \ b_{r-1}$

We can derive r quasigroup equations from Table 1 with indeterminate $x_0, x_1, \ldots, x_{r-1}$, but the form of those equations is quite complicated. Example 1 shows how the system of equations looks like for a simple case when r = 3.

 \Box Example 1. Let r = 3. Then at first we have the follow-

ing equalities from Table 1:

From them, we obtain the following system of quasigroup equations with indeterminate x_0, x_1, x_2 :

$$\begin{cases} b_0 = x_0 * (x_1 * (x_2 * x_0)) \\ b_1 = b_0 * ((x_1 * (x_2 * x_0)) * ((x_2 * x_0) * x_1)) \\ b_2 = b_1 * (((x_1 * (x_2 * x_0)) * ((x_2 * x_0) * x_1)) * \\ & * (((x_2 * x_0) * x_1) * x_2)). \end{cases}$$

Since the above system is enough simple, one can show that for any given $x_0 \in Q$ there are uniquely determined values of x_1 and x_2 . Thus, the system has $|Q| = n = n^{\lfloor 3/3 \rfloor}$ different solutions (x_0, x_1, x_2) . After that we have to check which one of the solutions is a preimage of (b_0, b_1, b_2) .

We notice here that the expressions $x_i^{(j)}$ are quite complicated when $i \approx j$, and in this case they contain exponential number of terms. Hence, even solving a system of equations for not so large values of r is not effective.

3 Edon–*R* Hash Algorithm

Having one-way quasigroup function \mathcal{R}_1 , we now define a hash algorithm named "Edon– \mathcal{R} " that map a message Mof arbitrary length of $l \leq 2^{64}$ words (where by word we mean a *w*-bit word) into a hash value of N words. The number N is the input parameter as it is the message Mas well.

The definition of Edon– \mathcal{R} hash function includes one string of length 2N and one string of length N, where N is the desired hash digest size in w-bit words. Those strings are the following:

1) String $H = (h_0, h_1, \dots, h_{2N-1})$ of length 2N that holds intermediate values of hashing H_i . The initial value H_0 is

$$H_0 = (h_0, h_1, \dots, h_{2N-1})$$

= $(0 \mod 2^w, 1 \mod 2^w, \dots, 2N - 1 \mod 2^w).$

2) The string $M_i = (m_0, m_1, \ldots, m_{N-1})$ which holds the *i*-th part of the padded message M'. More concretely, having a message $M = b_1 ||b_2|| \ldots ||b_j|| \ldots ||b_l$ that has length of l w-bit words, (we use the symbol || for operation of concatenation) by padding the message we will produce a new message $M' = b_1 ||b_2|| \dots ||b_j|| \dots ||b_l||l_1||l_2|| \dots ||l_t||b_1|| \dots ||b_j|$, where $l_1 ||l_2|| \dots ||l_t$ is the *w*-bit conjugate of the number l (l is considered as 64 bit number and $t = \lfloor \frac{64}{w} \rfloor + 1$), and $j \ge 0$ is the smallest nonnegative integer such that $l + t + j \equiv 0 \mod N$. Then, we will denote the parts of N consecutive *w*-bit words as

$$M' \equiv M_1 ||M_2|| \dots ||M_k|$$

where $k = \frac{l+t+j}{N}$, and the length of every M_i is N w-bit words.

Note that our padding is very similar with popular Merkle-Damgård strengthening, but still it differs from it by the usage of message bits from its beginning.

Edon– \mathcal{R} algorithm

Input: (Q, *), N and M, where: (Q, *) is a shapeless quasigroup of order $2^w, w \ge 4$, the number N is such that the length of the hash output is $w \times N$ bits and M is the message to be hashed.

Output: A hash of length $w \times N$ bits.

- 1. Pad the message M, so the length of the padded message M' is multiple of N w-bit words i.e. $|M'| = k \times N$.
- **2.** Initialize $H_0 = (0 \mod 2^w, 1 \mod 2^w, \dots, 2N 1 \mod 2^w)$.
- **3.** Compute the hash with the following iterative procedure:

For
$$i = 1$$
 to k do
 $H_i = \mathcal{R}_1(H_{i-1} || M_i) \mod 2^{2wN};$

Output:

$$Edon-\mathcal{R}(M) = H_k \mod 2^{wN}$$

The one-way function \mathcal{R}_1 in this concrete realization is considered as transformation $Q^{3N} \to Q^{3N}$ (i.e., we take r = 3N) and then, for obtaining the intermediate value H_i , we just apply the operation mod 2^{2wN} that takes the last 2N w-bit words from the result of \mathcal{R}_1 . Finally, since the requested output from the hash function is N w-bit words, we take just the last N w-bit words from the H_k and that is denoted as the operation mod 2^{wN} .

The doubling of the internal memory in our design is done by the fact that in every iterative step of its compression function, the strings of length 3N are mapped to strings of length 3N and then only the last significant 2N letters are kept for the next iterative step. Thus, by similar discussion as in the previous subsection (on the infeasibility of solving nonlinear quasigroup equations in shapeless quasigroups) we can claim that the workload for finding preimages and second-preimages for any hash function of the family Edon- \mathcal{R} is 2^N hash computations. Table 4: Experimental testing of the Birthday attack on Edon- \mathcal{R} with differen number of n bits of the hash output. The column $T = 2^{\frac{n}{2}}$ is the theoretically expected number of messages before finding a collision and the column Edon- \mathcal{R} shows the actual number of random messages that we examined before finding a collision.

n	$T = 2^{\frac{n}{2}}$	$\operatorname{Edon-} \mathcal{R}$
	o12 / ooo	
24	$2^{12} = 4,096$	6,258
32	$2^{16} = 65,536$	$50,\!681$
40	$2^{20} = 1,048,576$	1,023,436
48	$2^{24} = 16,777,216$	$19,\!280,\!524$

Table 5: Experimental examination of the avalanche criterion. The first column labelled as n is the number of bits of the hash output. The column Edon- \mathcal{R} has the calculated average Hamming distance \bar{H} and standard deviation σ .

n	$\operatorname{Edon-} \mathcal{R}$		
40	$\bar{H} = 20.00, \sigma = 3.16$		
64	$\bar{H} = 32.00, \sigma = 4.00$		
80	$\bar{H} = 40.00, \sigma = 4.47$		
128	$\bar{H} = 64.00, \sigma = 5.66$		
160	$\bar{H} = 79.89, \sigma = 7.03$		
256	$\bar{H} = 128.00, \sigma = 8.01$		
320	$\bar{H} = 159.39, \sigma = 13.27$		
384	$\bar{H} = 191.43, \sigma = 14.34$		
512	$\bar{H} = 255.97, \sigma = 11.31$		
1024	$\bar{H} = 512.00, \sigma = 16.01$		

3.1 Some Initial Experimental Tests of Edon $-\mathcal{R}$

The Edon– \mathcal{R} 's property to give hash outputs with variable lengths can be used to test Edon– \mathcal{R} algorithms for collisions and resistance on "Birthday attack". With the C reference implementation of Edon- \mathcal{R} we have chosen the number of output bits to be relatively small (24, 32, 40 and 48 bits), since for those cases we could examine all possibilities with modern PC computer in reasonable time. The number of examined input messages before finding a collision is shown in Table 4 and it is in compliance with the "Birthday attack" level of $2^{\frac{n}{2}}$, where n is the number of bits in the hash output.

We have also examined the avalanche property of Edon– \mathcal{R} family of hash functions. Namely, we have made experiments measuring Hamming distance of the hash outputs when original messages differ in 1 bit. For that purpose for every n bits ($n \in \{40, 64, 80, 128, 160, 256, 320, 384, 512, 1024\}$) we generated 1,000,000 pairs of messages (M_1, M_2) with random length from 100 to 1100

bytes and the difference between M_1 and M_2 was 1 bit (the position of that one bit was also randomly chosen). The summary of the experiments is presented in Table 5. As it is shown on Table 5, Edon– \mathcal{R} hash functions posses expected avalanche property, i.e., when two messages differ only in one bit, the expected Hamming distance between their *n*-bit hashes should be approximately $\frac{n}{2}$.

3.2 Speed Analysis of the Algorithm

If we look at the definition of Edon– \mathcal{R} , we will notice that the operations are mostly operations to determine memory location and the value in that location, (i.e., references to one dimensional or two dimensional arrays). So, from the perspective of the design of other hash functions (that are using mostly bit operations in registers, but also have references to one dimensional arrays) we can say that Edon– \mathcal{R} is using slower approach, because operations with memory are several times slower then operations with registers. Nevertheless, we think that the properties to have variable lengths of hash output, to have stronger mathematical indications about function's strong collision resistance, as well as the fact that we have infinite number of cryptographically strong hash functions, is worthwhile. Moreover, modern microprocessors have enough cash memory of Level 1, working with the speed of the microprocessor, for storing the whole quasigroup (of order 64×64 , or 256×256) and other one-dimensional strings of Edon– \mathcal{R} , so the operations can be executed inside the microprocessor and its Level 1 cash memory.

Another approach that can further increase the speed of execution of Edon– \mathcal{R} can be in design of hardware implementation in a special microprocessor that will execute its compression function \mathcal{R}_1 in parallel, reducing the execution time to O(N) microprocessor cycles.

4 Conclusions

We have defined an infinite class of one-way hash functions Edon– \mathcal{R} with variable length of output. The cryptographic strength of Edon– \mathcal{R} hash functions relies on conjectured computational infeasibility to solve system of quasigroup equations in shapeless quasigroups.

The property of having possibility to choose the length of hash output can be used in implementation of the applications that require hashes that have lengths different then 128, 160, 256, 384 or 512 bits. With Edon- \mathcal{R} you can produce hash outputs that are 11 or 10111 bytes long. If some day, the computing power of the computers would allow to find collisions of 20 bytes long hashes by brute force, then the design of Edon- \mathcal{R} will not change. Only the output of the hash functions will be increased.

Edon– \mathcal{R} is not a single cryptographic hash function but a huge (infinite) class of cryptographic hash functions, and that fact can be effectively used in the design of different cryptographic protocols. The compression function \mathcal{R}_1 of Edon– \mathcal{R} can be implemented in hardware in parallel manner and then its execution time can be reduced only to O(N) microprocessor cycles.

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